

First Named Inventor	Phillip G. Wald	<p align="center"><b><u>COMMUNICATION</u></b>  <b><u>REGARDING DIVISIONAL</u></b>  <b><u>APPLICATION</u></b></p>
Serial No.	Unknown	
Filing Date	Filed Herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Attorney Docket No.	400.116US02	
Title: ROM EMBEDDED DRAM WITH DIELECTRIC REMOVAL/SHORT		

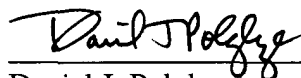
Mail Stop: Patent Application  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

The parent of the above-identified patent application, i.e., U.S. Patent Application Serial No. 09/992,203, was subjected to a Restriction Requirement by Examiner Nam Thanh Nguyen in Paper No. 4 of the parent application mailed February 21, 2003.

Claims 1-15 of the present divisional application are identical to unelected claims 11-25 of the parent application. No new matter has been added.

Respectfully submitted,

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